

Appl. No. 10/661,492
Reply to Office action of November 16, 2007

Amendments to the Specification

Please replace the first paragraph on page 5 in the specification as follows:

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...point of the external impedance sequentially. The three voltages at the dividing point are converted into three digital signals through an analog to digital converter (ADC) 14. Eventually, the three digital signals are sent to the controlling device 11 as a basis for I/O determination.

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Please replace the third paragraph on page 8 in the specification as follows:

Fig. 4 is a circuit diagram of the second embodiment of the invention. As shown, the detecting device 401 comprises three resistors (R3~R5) and a switch (SW1), and the
15 detecting device 401 separately connects the three resistors (R3~R5) with the impedance of the external device in series, and then sends the corresponding voltage values sequentially to an analog-to-digital converter (ADC) 404 and a controlling device 402 (as shown in fig. 3) of AC'97 chip 400 for determination. In the present embodiment, the type of the external device determination is accomplished by the software program.

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